

READ BACK multi port off (old scheme) (Register 0x1E(30) bit 1 = '0')

WIB

WRITE PORT = 0x7D00

READ PORT = 0x7D01

READ BACK multi port off = 0x7D02

HIGH SPEED RB 0x7D03

FEMB 1

WRITE PORT = 0x7D10

READ PORT = 0x7D11

READ BACK PORT = 0x7D12

FEMB 2

WRITE PORT = 0x7D20

READ PORT = 0x7D21

READ BACK PORT = 0x7D22

FEMB 3

WRITE PORT = 0x7D30

READ PORT = 0x7D31

READ BACK PORT = 0x7D32

FEMB 4

WRITE PORT = 0x7D40

READ PORT = 0x7D41

READ BACK PORT = 0x7D42

READ BACK multi port ON (Register 0x1E(30) bit 1 set)

WIB

WRITE PORT = 0x7D00

READ PORT = 0x7D01

READ BACK multi port on = 0x7D10 + IP address lower octant

HIGH SPEED RB 0x7D03

FEMB 1

WRITE PORT = 0x7900

READ PORT = 0x7901

READ BACK PORT = 0x7910 + IP address lower octant

FEMB 2

WRITE PORT = 0x7A00

READ PORT = 0x7A01

READ BACK PORT = 0x7A10 + IP address lower octant

FEMB 3

WRITE PORT = 0x7B00

READ PORT = 0x7B01

READ BACK PORT = 0x7B10 + IP address lower octant

FEMB 4

WRITE PORT = 0x7C00

READ PORT = 0x7C01

READ BACK PORT = 0x7C10 + IP address lower octant